

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1576	(257/355).CCLS.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:29
L2	643	(257/356).CCLS.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:29
L3	268	(257/361).CCLS.	US:PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:29
L4	455	(257/362).CCLS.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:29
L5	396	(257/363).CCLS.	US:PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:29
L6	53	(257/112).CCLS.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:29
L7	50	(257/111).CCLS.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:29
L8	387	(257/107).CCLS.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:30
L9	164	(257/109).CCLS.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:30
L10	484	(257/173).CCLS.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/04/18 09:30
L11	16	(ESD electrostatic adj discharge overvoltage) and CMOS near3 (triple adj well)	US:PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/18 09:35
L12	106	(ESD electrostatic adj discharge overvoltage) and (triple adj well)	US:PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/18 09:32
L13	19	SCR and triple adj well	US:PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/18 09:35